

	L #	Hits	Search Text	Time Stamp
1	L1	13463	fifo	2000/03/26 17:13
2	L2	182129	operat\$3 near2 mode\$1	2000/03/26 17:36
3	L3	751	1 same 2	2000/03/26 17:36
4	L4	3180	register near8 2	2000/03/26 17:36
5	L6	39	1 same 4	2000/03/26 17:43
6	L7	3244	configuration near2 register\$1	2000/03/26 17:46
7	L8	776	1 and 7	2000/03/26 17:46
8	L9	225	2 same 7	2000/03/26 17:46
9	L10	95	1 and 9	2000/03/26 17:46
10	L12	21	9 same (full or empty or fullness or emptiness or fill)	2000/03/26 17:48
11	L13	1138	node near3 register\$1	2000/03/26 17:49
12	L14	62745	(configuration or control or node or destination or target) near3 (register\$1 or bit\$1 or flag\$1)	2000/03/26 18:14
13	L15	6618	1 and 14	2000/03/26 18:14
14	L16	6422	14 same (full or fullness or fill or emptiness or empty or threshold)	2000/03/26 18:15
15	L17	3152	14 same (destination or target) near3 (address\$2 or node\$1)	2000/03/26 18:19
16	L18	3532	2 same 14	2000/03/26 18:19
17	L19	8016	2 same (receiver or transmitter or transceiver)	2000/03/26 18:20
18	L20	2632	1 and (16 or 17 or 18 or 19)	2000/03/26 18:20

	L #	Hits	Search Text	Time Stamp
19	L22	10	(18 and 17 and 16 and 19 AND 1)	2000/03/26 18:22
20	L23	676	1 same (16 or 17 or 18 or 19)	2000/03/26 18:23
21	L24	128	1 same (16 or 17) and (18 or 19)	2000/03/26 18:23
22	L26	38	((17 OR 16) SAME 1) same (18 OR 19))	2000/03/26 18:23

	Document ID	Issue Date	Title Δ	Current OR
1	US 5673396 A	19970930	Adjustable depth/width FIFO buffer for variable width data transfers	710/127
2	US 5889948 A	19990330	Apparatus and method for inserting an address in a data stream through a FIFO buffer	709/213
3	US 5689438 A	19971118	Audio bit stream generator for high definition television utilizing personal computer, and control method therefor	700/94
4	US 5295246 A	19940315	Bidirectional FIFO buffer for interfacing between two buses of a multitasking system	710/34
5	US 5255239 A	19931019	Bidirectional first-in-first-out memory device with transparent and user-testable capabilities	365/221

	Document ID	Issue Date	Title Δ	Current OR
6	US 5129079 A		Computer system having subinstruction surveillance capability	712/211

	Document ID	Issue Date	Title Δ	Current OR
7	US 4335277 A	19820615	Control interface system for use with a memory device executing variable length instructions	704/258
8	US 5381538 A	19950110	DMA controller including a FIFO register and a residual register for data buffering and having different operating modes	711/156

	Document ID	Issue Date	Title Δ	Current OR
9	US 5745731 A	19980428	Dual channel FIFO circuit with a single ported SRAM	711/149
10	US 5977791 A	19991102	Embedded memory block with FIFO mode for programmable logic device	326/40
11	US 5537607 A	19960716	Field programmable general purpose interface adapter for connecting peripheral devices within a computer system	710/10
12	US 4796203 A	19890103	High resolution monitor interface and related interfacing method	345/516
13	US 5818607 A	19981006	Image signal processing apparatus and information transmission/reception apparatus	358/426
14	US 5361363 A	19941101	Input/output system for parallel computer for performing parallel file transfers between selected number of input/output devices and another	712/22
15	US 5544326 A	19960806	selected number of processing nodes interface and control circuit for regulating data flow in a SCSI initiator with multiple host bus interface selection	710/129

	Document ID	Issue Date	Title Δ	Current OR
16	US 4761732 A		Interrupt controller arrangement for mutually exclusive interrupt signals in data processing systems	710/263
17	US 5903619 A	19990511	Method and apparatus for detecting a binary pattern in a serial transmission	375/366
18	US 5742647 A	19980421	Method and apparatus for detecting a binary pattern in a serial transmission	375/366
19	US 5918024 A	19990629	Method and apparatus for providing single channel communications	710/106
20	US 5617118 A	19970401	Mode dependent minimum FIFO fill level controls processor access to video memory	345/516

	Document ID	Issue Date	Title Δ	Current OR
21	US 4870704 A		Multicomputer digital processing system	710/120
22	US 5353412 A	19941004	Partition control circuit for separately controlling message sending of nodes of tree-shaped routing network to divide the network into a number of partitions	709/243

	Document ID	Issue Date	Title Δ	Current OR
23	US 4084228 A		Process management structures and hardware/firmware control	709/103

	Document ID	Issue Date	Title Δ	Current OR
24	US 4835733 A	19890530	Programmable access memory	711/218
25	US 6014764 A	20000111	Providing test vectors with pattern chaining definition	714/738
26	US 4611283 A	19860909	Radiographic imaging apparatus and methods for operating such apparatus	250/363.07

	Document ID	Issue Date	Title Δ	Current OR
27	US 4201105 A	19800506	Real time digital sound synthesizer	84/606
28	US 4302782 A	19811124	Reproduction scanning system having intermediate storage between input and output scanning stations	358/296
29	US 4205350 A	19800527	Reproduction scanning system having intermediate storage between input and output scanning stations	358/296
30	US 4169275 A	19790925	Reproduction scanning system having intermediate storage between input and output scanning stations	358/300
31	US 5655147 A	19970805	SCSI host adapter integrated circuit utilizing a sequencer circuit to control at least one non-data SCSI phase without use of any processor	710/7
32	US 5305379 A	19940419	Semiconductor integrated device	379/410

	Document ID	Issue Date	Title Δ	Current OR
33	US 5167020 A	19921124	Serial data transmitter with dual buffers operating separately and having scan and self test modes	711/119
34	US 5606673 A	19970225	Small computer system interface (SCSI) controller	710/129
35	US 4581757 A	19860408	Speech synthesizer for use with computer and computer system with speech capability formed thereby	704/258
36	US 5406554 A	19950411	Synchronous FIFO having an alterable buffer store	370/381

	Document ID	Issue Date	Title Δ	Current OR
37	US 4071887 A		Synchronous serial data adaptor	710/129
38	US 5515329 A	19960507	Variable-size first in first out memory with data manipulation capabilities	365/221

	Document ID	Issue Date	Title Δ	Current OR
39	US 5870391 A	19990209	Wireless communication system using frequency hopping, and method of controlling the system	370/330